

# United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	· ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/517,541	03/02/2000	Kia Silverbrook	AUTH15US	4978
7	590 02/11/2005		EXAM	INER
Kia Silverbrook			NGUYEN, NGA B	
Silverbrook Re	search Pty Ltd			
393 Darling Street			ART UNIT	PAPER NUMBER
Balmain, 2041			3628	
AUSTRALIA			DATE MAIL ED: 02/11/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

			4			
		Application No.	Applicant(s)			
		09/517,541	SILVERBROOK ET AL.			
Office .	Action Summary	Examiner	Art Unit			
		Nga B. Nguyen	3628			
	NG DATE of this communication app	ears on the cover sheet with the c	orrespondence address			
Period for Reply		VIC OFT TO EVOIDE A MONTH!	(C) FDOM			
THE MAILING DA  - Extensions of time ma after SIX (6) MONTHS  - If the period for reply s  - If NO period for reply if Failure to reply within any reply received by	STATUTORY PERIOD FOR REPLY ATE OF THIS COMMUNICATION. y be available under the provisions of 37 CFR 1.13 from the mailing date of this communication. specified above is less than thirty (30) days, a reply s specified above, the maximum statutory period version that the set or extended period for reply will, by statute the Office later than three months after the mailing justment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be ting within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).			
Status						
1) Responsive	Responsive to communication(s) filed on 22 September 2004.					
2a) This action	This action is <b>FINAL</b> . 2b)⊠ This action is non-final.					
3) Since this a	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in ac	ccordance with the practice under E	Ex parte Quayle, 1935 C.D. 11, 45	53 O.G. 213.			
Disposition of Claim	<b>IS</b>					
4)⊠ Claim(s) <u>1-</u>	8 is/are pending in the application.					
4a) Of the a	4a) Of the above claim(s) is/are withdrawn from consideration.					
5) Claim(s)	is/are allowed.					
6)⊠ Claim(s) <u>1-</u>	<u>8</u> is/are rejected.		•			
7) Claim(s)	is/are objected to.					
8)☐ Claim(s)	are subject to restriction and/o	r election requirement.				
Application Papers						
9) The specific	ation is objected to by the Examine	r.				
10) The drawing	ı(s) filed on is/are: a)□ acc	epted or b) $\square$ objected to by the I	Examiner.			
Applicant ma	ay not request that any objection to the	drawing(s) be held in abeyance. See	e 37 CFR 1.85(a).			
Replacemen	t drawing sheet(s) including the correct	ion is required if the drawing(s) is ob	jected to. See 37 CFR 1.121(d).			
11)∏ The oath or	declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.			
Priority under 35 U.S	S.C. § 119					
12) Acknowledg	ment is made of a claim for foreign	priority under 35 U.S.C. § 119(a)	)-(d) or (f).			
1.☐ Certif	fied copies of the priority documents	s have been received.				
2.☐ Certif	fied copies of the priority documents	s have been received in Applicati	on No			
3.☐ Copie	es of the certified copies of the prior	rity documents have been receive	ed in this National Stage			
applic	cation from the International Bureau	u (PCT Rule 17.2(a)).				
* See the attac	thed detailed Office action for a list	of the certified copies not receive	ed.			
AMachana at N						
Attachment(s)  1) Notice of Reference:	s Cited (PTO-892)	4) Interview Summary	(DTO 412)			
	on's Patent Drawing Review (PTO-948)	4) 🔛 Interview Summary Paper No(s)/Mail Da				
_	re Statement(s) (PTO-1449 or PTO/SB/08)	5) Notice of Informal P 6) Other:	atent Application (PTO-152)			

U.S. Patent and Trademark Office PTOL-326 (Rev. 1-04) Application/Control Number: 09/517,541 Page 2

Art Unit: 3628

#### **DETAILED ACTION**

This Office Action is the answer to the communication filed on September 22,
 which paper has been placed of record in the file.

2. Claims 1-8 are pending in this application.

## Response to Arguments/Amendment

3. Applicant's arguments with respect to claims 1-8 have been considered but are moot in view of new grounds of rejection.

### Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 1-8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Park, U.S. Patent No. 5,673,223.

Regarding to claim 1, Park discloses in an authentication chip in which secret data is manipulated, a method of shielding manipulations of the secret data from observation, including the steps of: operating non-flashing CMOS structures in the chip (columns 1-2 and column 4, lines 17-55).

Park does not disclose pMOS and nMOS transistors are driven such that they do not have intermediate resistance simultaneously during a change of state of the CMOS

Application/Control Number: 09/517,541 Page 3

Art Unit: 3628

structure, to manipulate the secret data and operating conventional CMOS inverters adjacent the non-flashing CMOS structures at the same time. However, designing pMOS and nMOS transistors are driven such that they do not have intermediate resistance simultaneously during a change of state of the CMOS structure, to manipulate the secret data and operating the conventional CMOS inverters adjacent the non-flashing CMOS structures at the same time, are well known in the art and are choices of designing and operating CMOS structures in the semiconductor. Therefore, it would have been obvious to one with ordinary skill in the art at the time the invention was made to modify Park's to incorporate the designed choices above for the purpose of improving the security in operation of an authentication chip in which secret data is manipulated

Regarding to claims 2-3, Park does not disclose generating continuous circuit noise to a tamper detection line and driving the conventional CMOS structures from the tamper detection line. However, generating continuous circuit noise to a tamper detection line and driving the conventional CMOS structures from the tamper detection line is well known in the art of semiconductor. Therefore, it would have been obvious to one with ordinary skill in the art at the time the invention was made to modify Park's to include the feature above for the purpose of generating continuous circuit noise in the conventional CMOS structures.

Regarding to claim 4, Park does not disclose driving the conventional CMOS multiple times faster than non-flashing CMOS. However, it is well known in the art to drive the conventional CMOS multiple times faster than non-flashing CMOS. This is a

Art Unit: 3628

desired choice. Therefore, it would have been obvious to one with ordinary skill in the art at the time the invention was made to modify Park's to include the feature above for the desired purpose.

Regarding to claims 5-8, Park discloses an authentication chip (figures 2-3) for performing the method as discussed in claims 1-4 above, therefore are rejected by the same rationale.

### **Conclusion**

- 6. Claims 1-8 are rejected.
- 7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to examiner Nga B. Nguyen whose telephone number is (703) 306-2901. The examiner can normally be reached on Monday-Thursday from 9:00AM-6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hyung S. Sough can be reached on (703) 308-0505.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 306-1113.

8. Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

C/o Technology Center 3600

Washington, DC 20231

Art Unit: 3628

Or faxed to:

(703) 872-9326 (for formal communication intended for entry),

or

(703) 308-3691 (for informal or draft communication, please label "PROPOSED" or "DRAFT").

Hand-delivered responses should be brought to Crystal Park 5, 2451 Crystal Drive, Arlington, VA, Seventh Floor (Receptionist).

Nga B. Nguyen

January 6, 2005